



PATENT
Attorney Docket No.: 728-208 (Y0R9-2001-0270 US)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: Louis L. HSU, et al. GROUP ART UNIT: 2814
APPLN NO: 09/880,223 EXAMINER: Rao, Shrinivas
FILING DATE: June 13, 2001 DATED: August 7, 2003
FOR: **T-RAM CELL HAVING A BURIED VERTICAL THYRISTOR
AND A PSEUDO-TFT TRANSFER GATE AND A METHOD
FOR FABRICATING THE SAME**

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

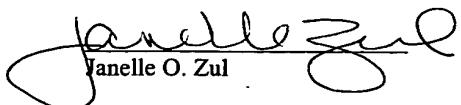
Sir:

In response to the Office Action of the United States Patent and Trademark Office dated May 7, 2003, please amend this application as follows:

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8 (a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postpaid in an envelope, addressed to the: Mail Stop Non-Fee Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on August 7, 2003.

Dated: August 7, 2003



Vanelle O. Zul